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IDS
J. Macmillan

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)
Akiyuki Yoshisato et al.)
Serial No. To Be Assigned)
Filing Date: Herewith)
For: Electronic Circuit Unit That is Suitable)
for Miniaturization and Excellent in)
High Frequency Characteristic)



INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

Pursuant to the obligation under 37 C.F.R. 1.56 and in conformance with 37 C.F.R. 1.97-1.99, Applicants hereby submit reference A1 listed on the attached form PTO-1449 for consideration by the Examiner. A copy of the reference is enclosed herewith

Japanese Unexamined Patent Publication No. Hei 6-53406 discloses a method for forming a thin film circuit element including a capacitor and an inductor element.

The filing of this Information Disclosure Statement does not constitute an admission that the information cited herein is, or is considered to be, material to patentability as defined in 37 C.F.R. Section 1.56(b). Further, Applicants reserve the right to contest this reference as prior art against the present application, and Applicants do not believe that the disclosure of this reference, even if finally determined to be prior art, anticipates Applicants' invention or that this reference makes Applicants' invention obvious.

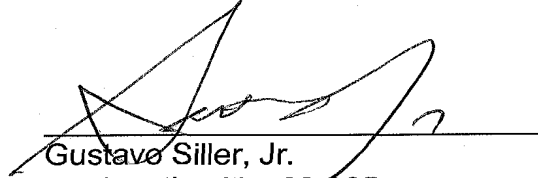
No fees are believed to be due in connection with filing of this Information Disclosure Statement, however, should any fees under 37 C.F.R. §§ 1.16 to 1.21 be

Consented by Douglas B. Vigor on 01/11/03.

deemed necessary for any reason relating to this material, the Commissioner is hereby authorized to deduct said fees from Brinks Hofer Gilson & Lione Deposit Account No. 23-1925.

Applicants respectfully request that the Examiner review the entire disclosure of this document and make it of record.

Respectfully submitted,



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